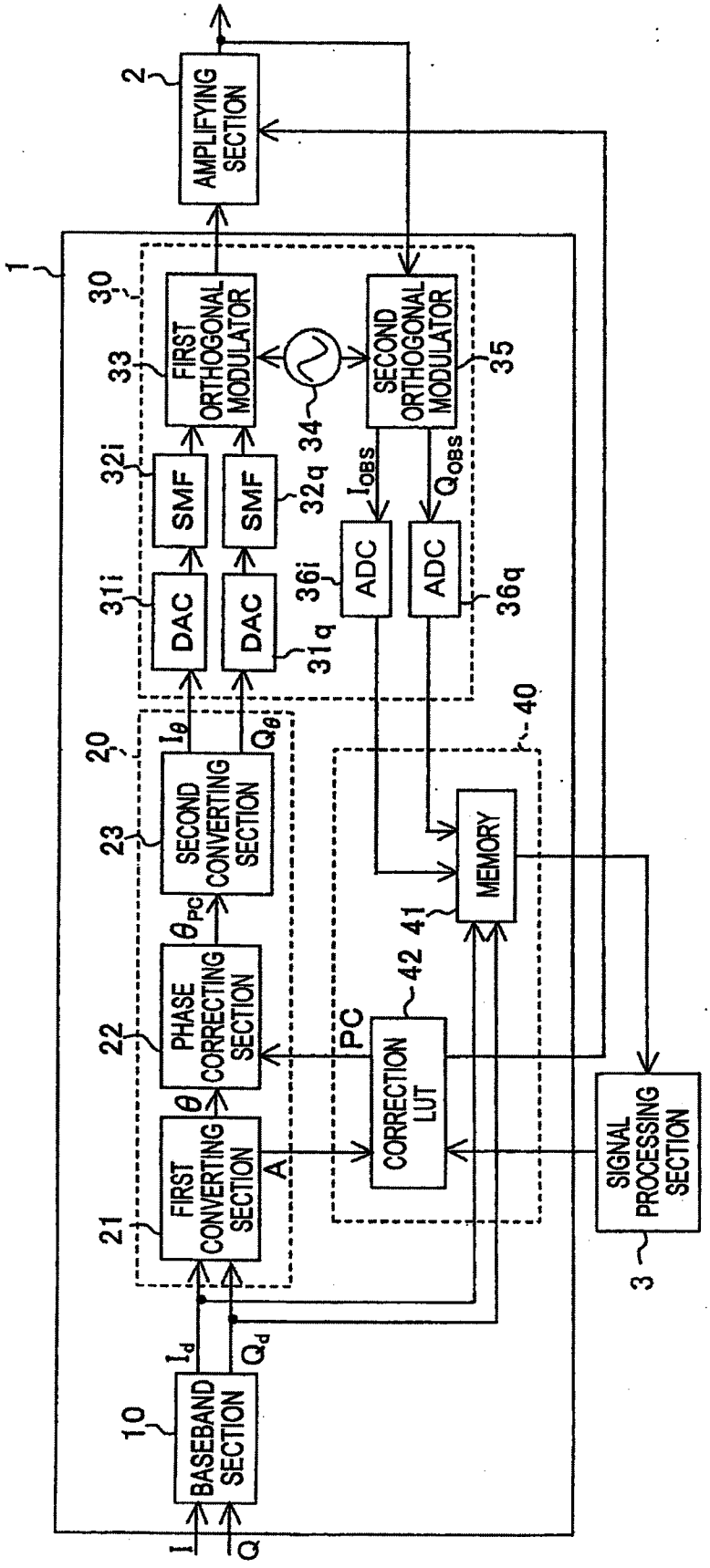
FIG. 1


1
2
AMPLIFYING SECTION
30
FIRST ORTHOGONAL MODULATOR
33
SECOND ORTHOGONAL MODULATOR
35
34
32i
SMF
32q
SMF
I_{OBS}
Q_{OBS}
31i
DAC
31q
DAC
36i
ADC
36q
ADC
40
20
I_θ
Q_θ
23
SECOND CONVERTING SECTION
41
MEMORY
θ_{PC}
22
PHASE CORRECTING SECTION
42
PC
CORRECTION LUT
θ
21
FIRST CONVERTING SECTION
A
SIGNAL PROCESSING SECTION
3
I_d
Q_d
10
BASEBAND SECTION
I
Q